

AX386

- Debugs Real to Protected Mode Transition
- Regular Series Chip Used - Easy Upgrades
- Low Cost Real-Time Emulator - 0 Wait States
- Sophisticated Trace Recording Using HLL Lines
- Sophisticated Triggers can be Combined
- Special Function Registers Displayed in English
- 20 Hardware Breakpoints
- Emulation Up to 25 MHz - 33 MHz 1Q '97
- Low Voltage Support - Using the Existing Probe
- Code Coverage and Performance Analysis

The AX386 dedicated emulator offers powerful Real-Time debugging for Intel386™ EX/SX microprocessors at a new low cost. The AX386 uses the same technology as the popular teletest32 emulator for the Intel386 DX/EX/SX processors.

The AX386 offers non-intrusive, real-time emulation of the target under test. No cycle-stealing, wait-state introduction or stopping of the CPU occurs. The Real to Protected mode transition (mixed mode) can be debugged with HiTOP. HiTOP allows the display and changing of the Hidden Register Cache and of the Test Registers. HiTOP displays long addresses, opcodes and mnemonics with the column configurable Instruction Window.

Breakpoints stop the emulation before executing the instruction where the breakpoint is set. They can be set in ROM as well as writable memory locations.

The Special Function Registers of the processor are displayed in plain English rather than bit patterns or hexadecimal codes. The need to consult data books or decode obscure codes is eliminated. These registers are easily changed within HiTOP with mouse clicks, and the user is prompted with the possible choices.

Complicated data structures are easily followed by merely clicking on the structure's name in the Examine Window. Structures within structures may be displayed by continuing this process. The debugging process is greatly simplified using this process.

Sophisticated filtering mechanisms for the trace function allow a smaller trace memory to be used freeing up resources for other important emulator features.

Processor cycles are stored in terms of the High Level Language source lines. A minimum of trace memory (only two frames per line) are used no matter how complicated the line is. Trace recording using the Raw Cycles Method is still available. Benefits are the ease of finding problem areas in your source code and the expanded trace regions and trace filtering features that are made possible with this Hitex system.

PROCESSORS SUPPORTED:
Intel386 EX/SX/CX processors

DEVELOPMENT PLATFORMS:
DOS, Windows* 3.1, Windows 95, Windows NT, HiNET Ethernet connection (1Q '97), UNIX (1Q '97)

AVAILABILITY:
Now

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